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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

TODD R. ABBOTT
MIKE VIOLETTE
ZHONGZE WANG
JIGISH D. TREVIDI

Serial No.: 10/780,014

Filed: February 17, 2004

For: METHOD OF USING HIGH-K DIELECTRIC
MATERIALS TO REDUCE SOFT ERRORS
IN SRAM MEMORY CELLS, AND A
DEVICE COMPRISING SAME

Patent No. 6,900,494 B2

Issue Date: May 31, 2005

Group Art Unit: 2814

Examiner: Tuan N. Quach

Att'y Docket: 2008.005582

Customer No.: 23720

Certificate

JUN 14 2005

of Correction

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING

I hereby certify that this paper or fee is being deposited with the United States Postal Service with sufficient postage as "FIRST CLASS MAIL" addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the 8th day of June, 2005.

Mary Paul
Signature

The patentee requests a certificate of correction for Patent and Trademark Office ("PTO") errors pursuant to 37 C.F.R. § 1.322. Because these errors were the PTO's, no fee should be required. If any fee is required, please charge Williams, Morgan & Amerson, P.C. Deposit Account No. 50-0786/2008.005582.

The following errors are listed on the attached form PTO-1050.

Col. 7, line 36 (claim 2, line 1), before "said" insert -- wherein --.

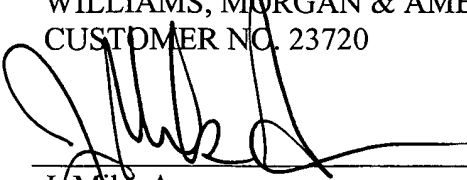
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Please date stamp and return the accompanying postcard to evidence receipt of this document.

Respectfully submitted,

WILLIAMS, MORGAN & AMERSON
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Date: June 8, 2005



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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,900,494 B2
 DATED : May 31, 2005
 INVENTOR(S) : Todd R. Abbott et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 7, line 36 (claim 2, line 1), before "said" insert -- wherein --.

MAILING ADDRESS OF SENDER:
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